ABOUT THE INSTITUTE

Prof. M. N. Navale embarked on his journey on 12th August 1993 with his academic qualifications, professional expertise, his mother's blessings and a thought, **'A burden of one's own choice is not felt'**. It is because he made this choice 20 years ago that we stand proud today as a part of this esteemed establishment called '**Sinhgad Technical Education Society**'. STES is now spread into 13 campuses with 97 institutes, more than 15 disciplines, around 4500 academic staff and 55000 students.

Sinhgad Institute of Technology and Science (SITS) was established in 2008 under the umbrella of Sinhgad Technical Education Society. SITS is approved by All India Council for Technical Education (AICTE), New Delhi, Directorate of Technical Education (DTE), Mumbai, and is affiliated to Savitribai Phule Pune University, Pune. The institute offers UG programme in Computer Engineering, Information Technology, Electronics and Telecommunication, Mechanical Engg., Civil Engg. And MBA. Also PG in E&TC Engg, Mech Engg, and MBA

ABOUT THE COURSE

This course is intended to train the faculties from various institutes on "CUDA and OpenMP" with following objectives:

- To introduce techniques / tools based on NVIDIA's CUDA environment.
- To provide special sessions with rich hands-on experience on various languages and tools (CUDA & OpenMP)

THE COURSE CONTENTS

DAY 1:
- ☑ Parallel Programming paradigm (Theory)
- ☑ CUDA SDK Installation (Hands-on Session)

DAY 2:
- ☑ CUDA Introduction (Theory)
- ☑ CUDA Programming model, CUDA Kernel
- ☑ Hands-on Problems (Sample Assignments)

DAY 3:
- ☑ Thread, warp and block, grid organization
- ☑ Implementation of parallel quick sort algorithm
- ☑ Odd even Sort Algorithm (Hands on Session)

DAY 4:
- ☑ CUDA Memory organization (Theory)
- ☑ N by N matrix multiplication (Hands on)
- ☑ Introduction to OpenMP (Theory & Hands on)

DAY 5:
- ☑ Introduction to OpenCL
- ☑ Program for read/write/modify distributed file using OpenCL (Hands on)

TECHNICAL PROGRAMME COMMITTEE:

Experienced professionals from academics & IT industry having 10+ years experience.

THE REGISTRATION

Limited to 50 participants on first-come-first-serve basis. Accommodation will be provided to needy participants by paying Rs 200/- per day. Course package includes Course material, Certificate, Stationery, Tea & Lunch.

Registration fees: Rs. 2500/-

Only Cash or DD will be accepted. The D.D. should be drawn in the name of **"SITS Department of Computer Engineering",** payable at Pune.

The completed application form in the prescribed format along with DD/Cash must reach to the coordinator on or before December 10, 2014..

Registration details are available at www.sinhgad.edu

Send registration details to: sadarade_sits@sinhgad.edu

Sinhgad Institutes

Sinhgad Technical Education Society's
Sinhgad Institute of Technology & Science
Department of Computer Engineering

ORGANIZES

Five Days FDP
On
"CUDA and OpenMP"
(December 17-21, 2014)

REGISTRATION FORM

Name: _____

Designation:_____

Institution: _____

_____

Address for Correspondence:_____

_____

Telephone:_____

Fax: _____

Email: _____

DD No:_____   Date: _____

Bank:_____   Branch:_____

Accommodation required: Yes/ No

Place: Date:

Signature of Applicant _____

Seal & Signature of Sponsor: _____

CHIEF PATRONS

Prof. M. N. Navale
Founder President, STE Society, Pune.

Dr. (Mrs.) Sunanda M. Navale
Founder Secretary, STE Society, Pune.

Mr. Rohit M. Navale
Vice President (HR), STE Society, Pune.

Ms. Rachana M. Navale
Vice President (Admin), STE Society, Pune.

Dr. A. V. Deshpande
Principal, SKNCOE, Director, STES
Member BOS (Computer Engineering),
Savitribai Phule Pune University, Pune.

Dr. S. N. Mali
Principal, SITS, Narhe

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PROF. U. C. AGASHE
HOD, MECH DEPT., SITS
PROF. D. E. UPASANI
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Prof H.B. PATIL
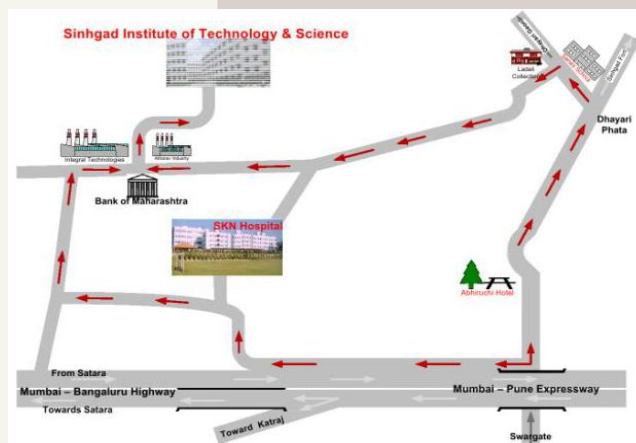HOD, MBA DEPT, STS

CONVENER

Prof. Mrs. G. S. Navale,
Head, Comp Engg, SITS, Narhe

ORGANISING COMMITTEE:

- Prof Nihar Ranjan — 9763722262
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LOCATION MAP:

Sinhgad Institute of Technology & Science

**Five Days FDP
On
"CUDA and OpenMP
(December 17-21, 2014)**

END of CPU ??
2,688 CUDA Cores   4,500 Gigaflops   7.1 Billion Transistors

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